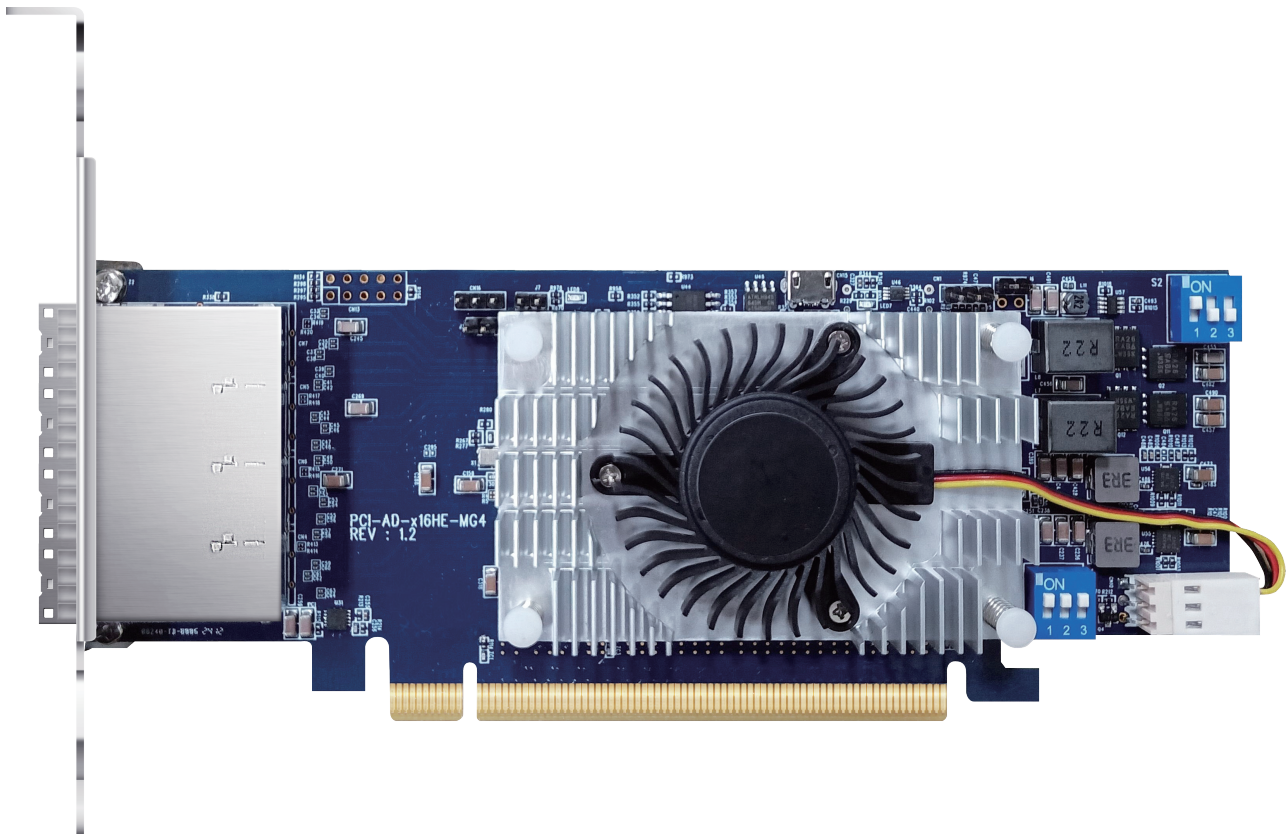


NS388P

NP980A-G4 host card



User Manual

First edition, Apr. 2025

Headers/Connector  

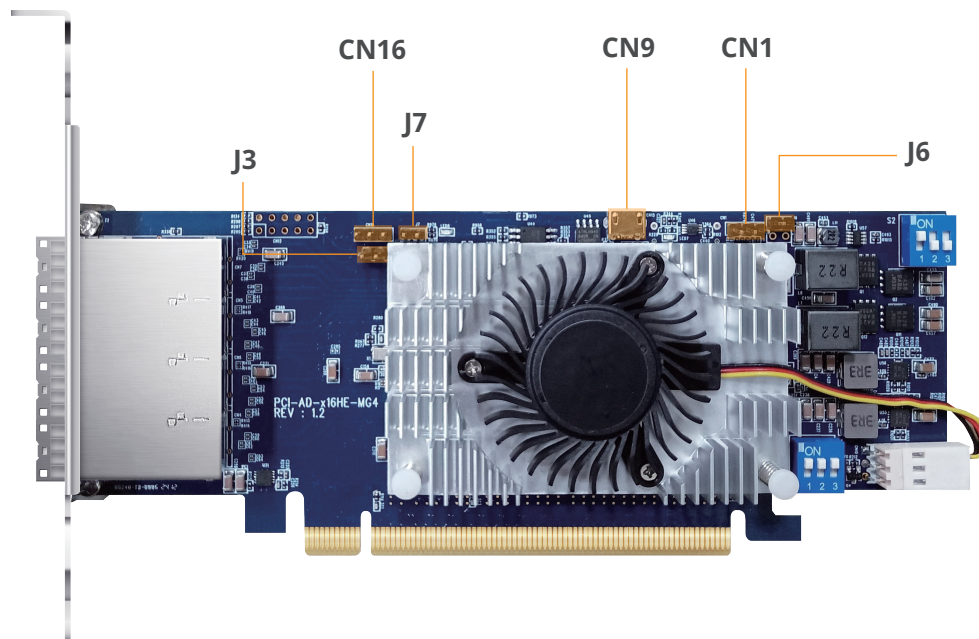
Side-band modes selection  

Bifurcation modes selection  

LEDs' Definition  

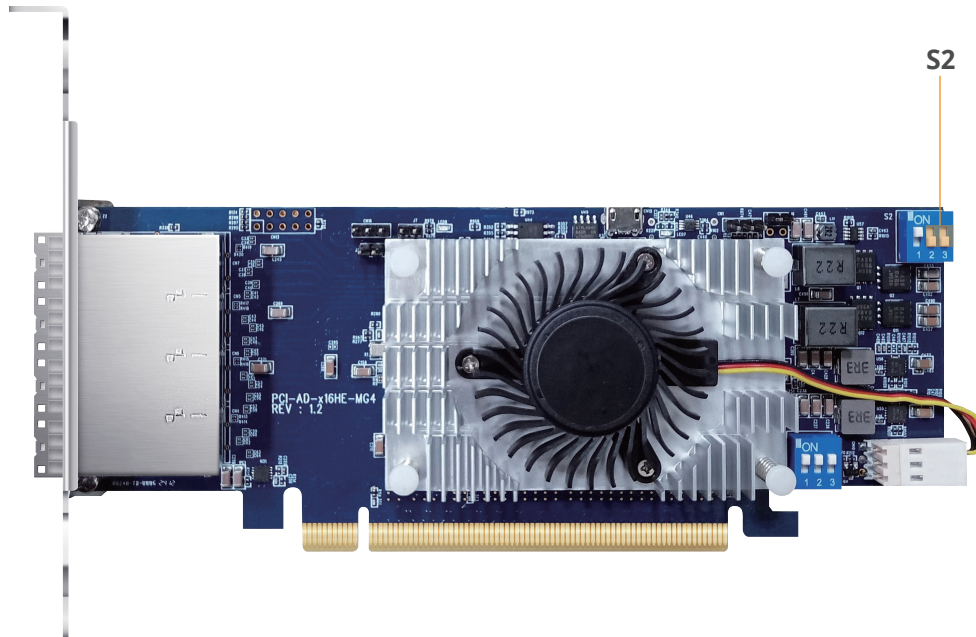
Ports' connecting sequence between enclosure and host card  

Headers/Connector



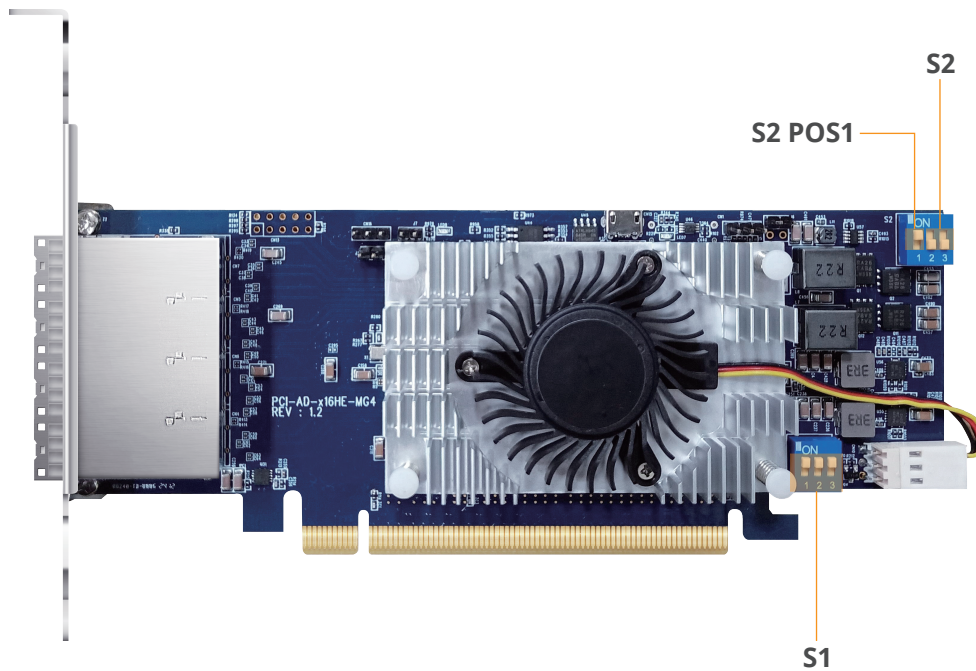
Headers	Description	Pinout
J6	ON: Force PCIe switch to enter boot recovery 1 OFF: PCIe switch loading default FW image as normal operation (default)	
CN1	ON: Force PCIe switch to enter boot recovery 1 OFF: PCIe switch loading default FW image as normal operation (default)	TX/RX/GND
CN9	Micro-USB port for executing uP CLI commands	
J7	ON: uP in firmware upgrading mode OFF: uP in normal operation mode (default)	
CN16	Reserved I/F for uP firmware debugging UART with 3.3V TTL signals level	TX/RX/GND
J3	ON: ISP mode for uP firmware programming OFF: uP in normal operation (default)	

Side-band modes selection



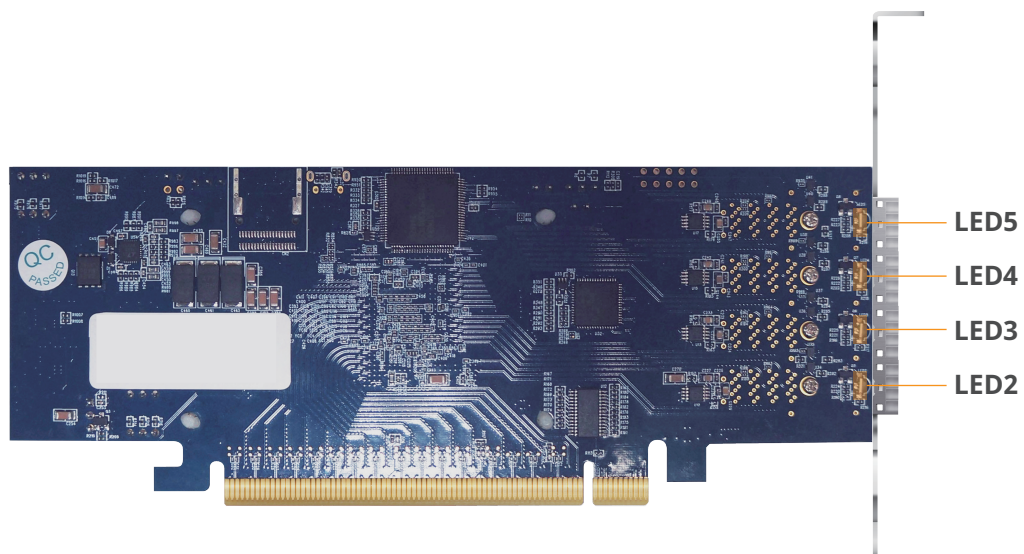
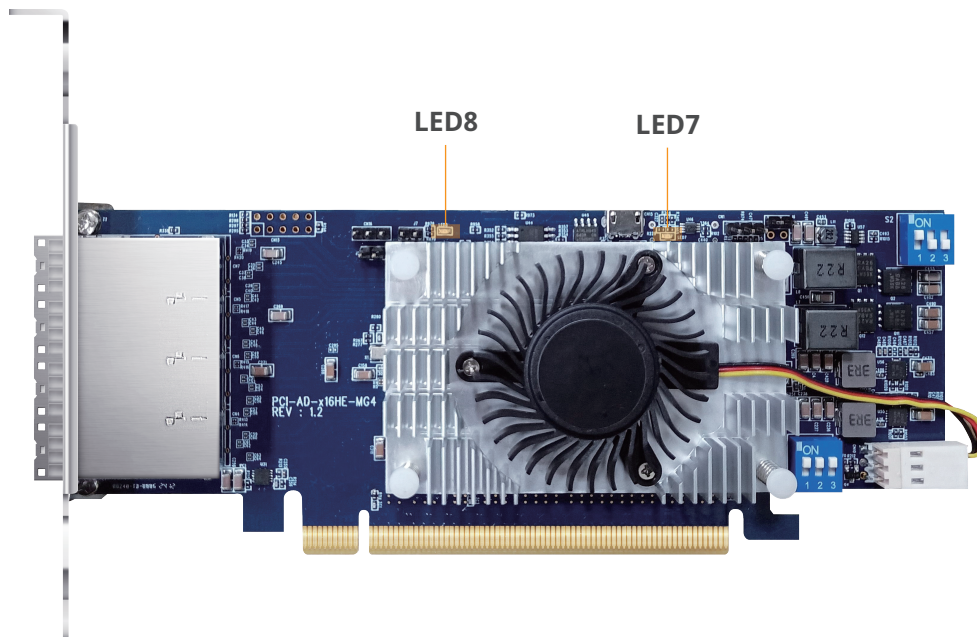
POS	Description
2 3	
	Target mode, and select Side-band mode to PCI-SIG for SFF-8674 connectors
	Host mode, and select Side-band mode to PCI-SIG for SFF-8674 connectors
	Host mode, and select Side-band mode to SC for SFF-8674 connectors

Bifurcation modes selection



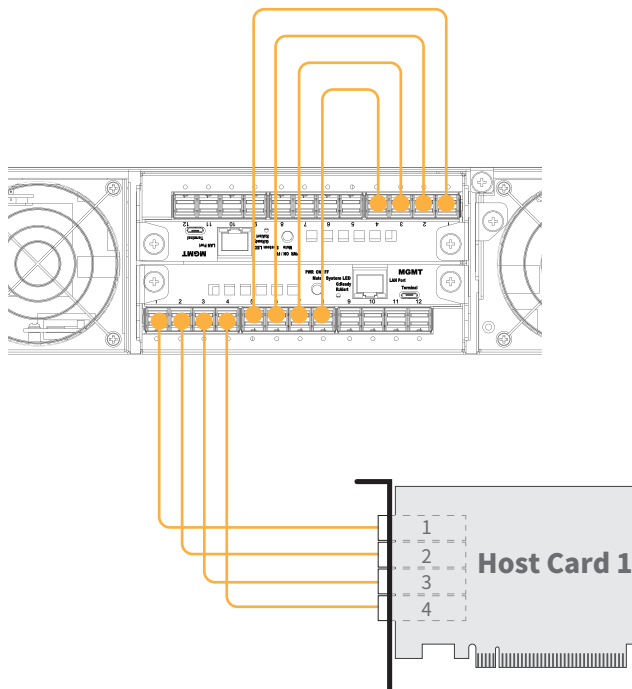
S2 POS	S1 POS	Mode	Host/Target	Description
1	1 2 3			
<input type="checkbox"/>	<input type="checkbox"/>	1	Host	SRNS: Set SFF-8674 to one x16 link
<input type="checkbox"/>	<input type="checkbox"/>	2		SRNS: Set SFF-8674 to Two x8 link
<input type="checkbox"/>	<input type="checkbox"/>	3		SRNS: Set SFF-8674 to Four x4 link
<input type="checkbox"/>	<input type="checkbox"/>	4		SRNS: Set SFF-8674 to Eight x2 link
<input type="checkbox"/>	<input type="checkbox"/>	5		SRIS: Set SFF-8674 to one x16 link
<input type="checkbox"/>	<input type="checkbox"/>	6		SRIS: Set SFF-8674 to Two x8 link
<input type="checkbox"/>	<input type="checkbox"/>	7		SRIS: Set SFF-8674 to Four x4 link
<input type="checkbox"/>	<input type="checkbox"/>	8		SRIS: Set SFF-8674 to Eight x2 link
<input type="checkbox"/>	<input type="checkbox"/>	9	Target	SRIS: Set SFF-8674 to one x16 link

LEDs' Definition



Location	Color	Definition
LED7	Blue	<p><u>PCIe switch heartbeat LED</u></p> <p>Blinking: Indicates PCIe switch loading firmware successfully and working correctly</p>
LED8	Green	<p><u>System health LED</u></p> <p>0.5Hz blinking when system is good</p> <p>2Hz blinking when any failure event detected, e.g. voltages, fan, and temp failed</p>
LED 5/4/ 3/2	Red	<p><u>Link matching LED for SFF-8674 connectors</u></p> <p>Case 1: set in mode 1, 5 or 9 LED5 lights when SFF-8674 ports not linking at x16</p> <p>Case 2: set in mode 2 or 6 LED5 and LED3 light when SFF-8674 ports not linking at x8</p> <p>Case 3: set in mode 3, 4, 7 or 8 LED5, LED4, LED3 and LED2 light when SFF-8674 ports not linking at x4 or 2x2</p>

Ports' connecting sequence between enclosure and host card



2×2 backplane & 1×4 backplane NS388P enclosure and host card connection diagram.

Enclosure's port 1 should be connected to host card's port 1, and then subsequently until enclosure's port 4 connecting to host card's port 4.

If you have any questions, please contact your regional distributor, or Netstor Technology, Taiwan.



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